

Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	1 / 25
Document No.		Revision	2.0

TO : Roadwell

Date : Nov., 06, 2012

HannStar Product Specification **(Formal)**

6.2" Color TFT-LCD Module

Model: HSD062IDW1-A02-0299

Note:

1. Please contact HannStar Display Corp. before designing your product based on this module specification.
2. The information contained herein is presented merely to indicate the characteristics and performance of our products. No responsibility is assumed by HannStar for any intellectual property claims or other problems that may result from application based on the module described herein.

Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	2 / 25
Document No.		Revision	2.0

Record of Revisions

Rev.	Date	Sub-Model	Description of change
1.0	Mar, 01, 2012	-	Formal Product Specification was first issued.
1.1	Oct, 19, 2012	-	Modify Item 3.1 Optical Specification_ Transmittance & color chromaticity (P.6) Item 8.1 Outline Dimension (P.20)
2.0	Nov, 06, 2012	-	Modify Item 8.1 Outline Dimension_ FPC Tolerance (P19)

Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	3 / 25
Document No.		Revision	2.0

Contents

1.0	General description	p.4
2.0	Absolute maximum ratings	p.5
3.0	Optical characteristics	p.6
4.0	Block diagram	p.9
5.0	Interface pin connection	p.10
6.0	Electrical characteristics	p.12
7.0	Reliability test items.....	p.18
8.0	Outline dimension	p.19
9.0	Lot mark	p.21
10.0	Package specification	p.22
11.0	General precaution	p.24

Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	4 / 25
Document No.		Revision	2.0

1.0 GENERAL DESCRIPTION

1.1 Introduction

HannStar Display model HSD062IDW1-A02-0299 is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel and a driving circuit. This TFT LCD has a 6.2 (16:9) inch diagonally measured active display area with 800 horizontal by 480 vertical pixel resolutions.

1.2 Features

- 6.2 (16:9 diagonal) inch configuration
- 6 bits + FRC driver with 1channel TTL interface
- Up/Down, Left/Right reversion selection
- RoHS/ Halogen Free Compliance

1.3 Applications

- Automotive

1.4 General information

Item	Specification	Unit
Outline Dimension	147 x 86.232 x 1.43 (Typ.)	mm
Display area	137.52(H) x 77.232(V)	mm
Number of Pixel	800 RGB (H) x 480(V)	pixels
Pixel pitch	0.1719(H) x 0.1609(V)	mm
Pixel arrangement	RGB Vertical stripe	
Display mode	Normally white	

Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	5 / 25
Document No.		Revision	2.0

2.0 ABSOLUTE MAXIMUM RATINGS

2.1 Electrical Absolute Rating

2.1.1 TFT LCD Module

Item	Symbol	Min.	Max.	Unit	Note
Power supply voltage	V _{CC}	-0.3	6.0	V	GND=0
	V _{GH}	-0.3	40	V	GND=0
	V _{GL}	-20	0.3	V	GND=0
	AV _{DD}	-0.5	15	V	AGND=0
	V _{COM}	0	6	V	
Logic Signal Input Level	V _I	-0.3	V _{CC} +0.3	V	

Note

- (1) Permanent damage may occur to the LCD module if beyond this specification.
Functional operation should be restricted to the conditions described under normal operating conditions.
- (2) Ta =25±2℃

2.2 Environment Absolute Rating

Item	Symbol	Min.	Max.	Unit	Note
Operating Temperature	T _{opa}	-20	70	℃	
Storage Temperature	T _{stg}	-30	80	℃	

Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	6 / 25
Document No.		Revision	2.0

3.0 OPTICAL CHARACTERISTICS

3.1 Optical specification

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Transmittance (With PZ)		T	Θ=0 Normal viewing angle	3.77	4.1	—	%	
Contrast		CR		480	600	—		(1)(2)
Response time	Rising	T _R		—	2	4	msec	(1)(3)
	Falling	T _F		—	6	12		
White luminance (Center)		Y _L		320	400	—	cd/m ²	(1)(4) (I _L =140mA)
Color chromaticity (CIE1931)	White	W _x		0.253	0.303	0.353		(1)(4) CF glass (C-light)
		W _y		0.274	0.324	0.374		
	Red	R _x		0.579	0.609	0.639		
		R _y		0.300	0.330	0.360		
	Green	G _x		0.257	0.287	0.317		
		G _y	0.497	0.527	0.557			
	Blue	B _x	0.117	0.147	0.177			
		B _y	0.108	0.138	0.168			
Viewing angle	Hor.	Θ _L	CR>10	65	75	—		
		Θ _R		65	75	—		
	Ver.	Θ _U		60	70	—		
		Θ _D		60	70	—		
Optima View Direction		6 O' clock						(5)

3.2 Measuring Condition

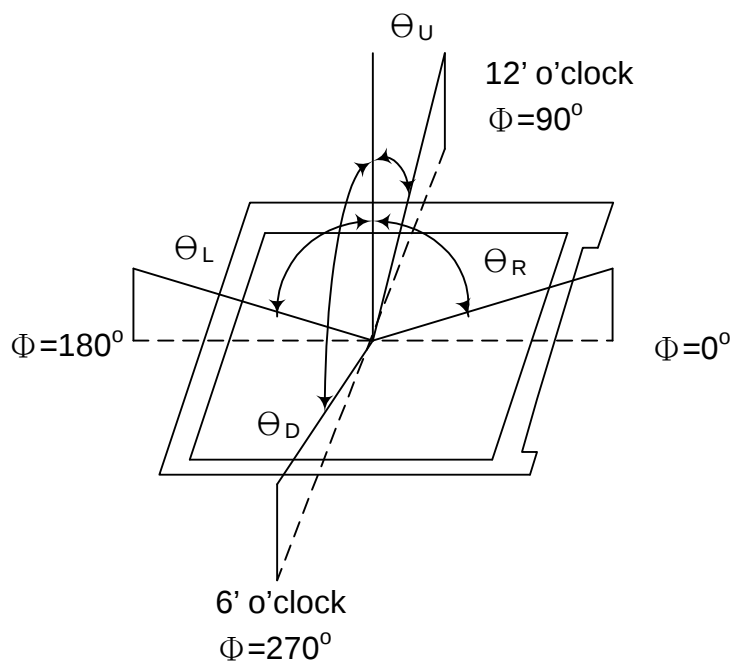
- Measuring surrounding: dark room
- LED current I_L : 140mA
- Ambient temperature: 25±2°C
- 15min. warm-up time.

3.3 Measuring Equipment

- FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.
- Measuring spot size: 20 ~ 21 mm

Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	7 / 25
Document No.		Revision	2.0

Note (1) Definition of Viewing Angle:

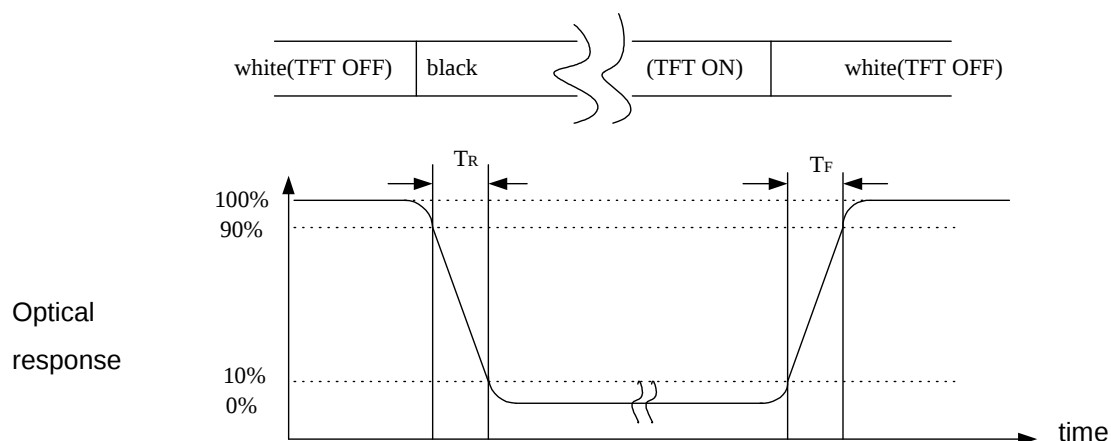


Note (2) Definition of Contrast Ratio (CR):
measured at the center point of panel

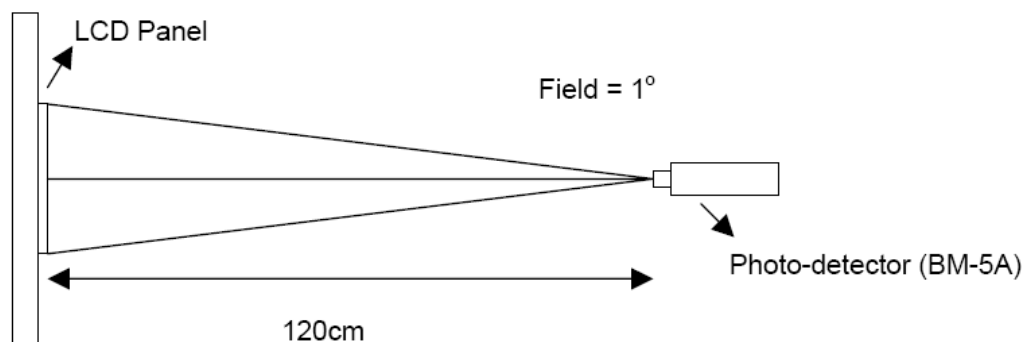
$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	8 / 25
Document No.		Revision	2.0

Note (3) Definition of Response Time : Sum of T_R and T_F

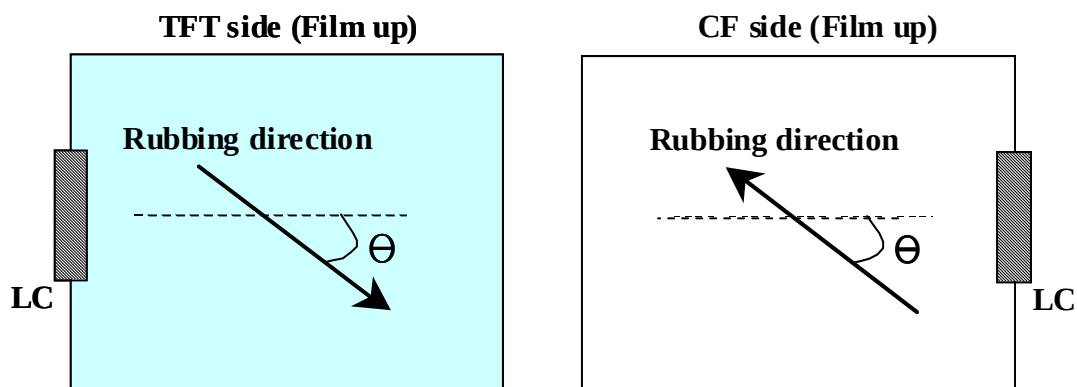


Note (4) Definition of optical measurement setup



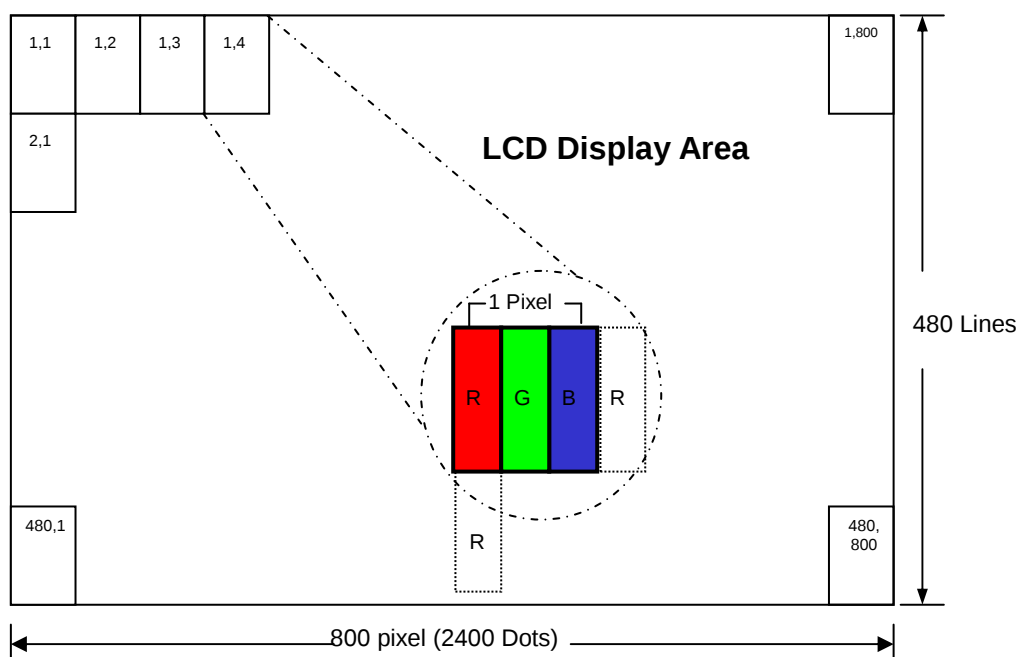
Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	9 / 25
Document No.		Revision	2.0

Note (5) Rubbing Direction (The different Rubbing Direction will cause the different Optima view direction.



4.0 BLOCK DIAGRAM

4.1 Pixel Format



Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	10 / 25
Document No.		Revision	2.0

5.0 INTERFACE PIN CONNECTION

5.1 TFT LCD Module

CN2 (Input signal): FPC Down Connector, (FH28-60S-0.5SH (HIROSE), 60pin,pitch = 0.5mm)

Terminal no.	Symbol	I/O	Function
1	AGND	P	Analog Ground
2	AVDD	P	Analog Power
3	VCC	P	Digital Power
4	R0	I	Data Input(LSB)
5	R1	I	Data Input
6	R2	I	Data Input
7	R3	I	Data Input
8	R4	I	Data Input
9	R5	I	Data Input
10	R6	I	Data Input
11	R7	I	Data Input(MSB)
12	G0	I	Data Input(LSB)
13	G1	I	Data Input
14	G2	I	Data Input
15	G3	I	Data Input
16	G4	I	Data Input
17	G5	I	Data Input
18	G6	I	Data Input
19	G7	I	Data Input(MSB)
20	B0	I	Data Input(LSB)
21	B1	I	Data Input
22	B2	I	Data Input
23	B3	I	Data Input
24	B4	I	Data Input
25	B5	I	Data Input
26	B6	I	Data Input
27	B7	I	Data Input(MSB)
28	DCLK	I	Clock input
29	DE	I	Data Enable signal
30	HSD	I	Horizontal sync input.Negative polarity
31	VSD	I	Vertical sync input.Negative polarity
32	MODE3	I	DE/SYNC mode select .normally pull high H:DE mode.L:HSD/VSD mode
33	RSTB	I	global reset pin.Active low to enter reset state.suggest to connecting with an RC reset circuit for stability .normally pull high.
34	STBYB	I	standby mode,normally pull high STBYB="1",normal operation STBYB="0",timming control ,soruce driver will turn off,all output are high-Z
35	SHLR	I	Source right or left sequence control.SHLR="L",shift left:last data=S1<-S2...S1200=first data SHLR="H",shift right:first data=S1->SS2...S1200=last data

Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	11 / 25
Document No.		Revision	2.0

Terminal no.	Symbol	I/O	Function
36	VCC	P	Digital Power
37	UPDN	I	gate up or down scan control. UPDN="L" , DOWN shift : G1->G2...->G480 ; UPDN="H", up shift: G1<-G2...<-G480
38	GND	P	Digital Ground
39	AGND	P	Analog Ground
40	AVDD	P	Analog Power
41	VCOMin	I	For external VCOM DC input (Adjustable)
42	DITH	I	Dithering setting: DITH="H" 6bit resolution (last 2 bits of input data truncated) (default setting) DITH="L" 8bit resolution
43	NC	-	Not connect For Test
44	NC	-	Not connect
45	V10	P	Gamma correction voltage reference
46	V9	P	Gamma correction voltage reference
47	V8	P	Gamma correction voltage reference
48	V7	P	Gamma correction voltage reference
49	V6	P	Gamma correction voltage reference
50	V5	P	Gamma correction voltage reference
51	V4	P	Gamma correction voltage reference
52	V3	P	Gamma correction voltage reference
53	V2	P	Gamma correction voltage reference
54	V1	P	Gamma correction voltage reference
55	NC	-	Not connect
56	VGH	P	Positive Power for TFT
57	VCC	P	Digital Power
58	VGL	P	Negative Power for TFT
59	GND	P	Digital Ground
60	NC	-	Not connect

Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	12 / 25
Document No.		Revision	2.0

6.0 ELECTRICAL CHARACTERISTICS

6.1 TFT LCD Module

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Supply Voltage	V _{CC}	3.0	3.3	3.6	V	
	V _{GH}	12	15	23	V	
	V _{GL}	-12	-7	-5	V	
	AV _{DD}	9.9	10	10.1	V	
VCOM	VCOMin	-	3.4	-	V	
Input signal voltage	V _{iH}	0.7 V _{CC}	-	V _{CC}	V	Note (1)
	V _{iL}	0	-	0.3 V _{CC}	V	
Current of power supply	I _{DD}	-	12.37	-	mA	V _{CC} =3.3V
	I _{ADD}	-	13.599	-	mA	AV _{DD} =10 V (Black)
	I _{GH}	-	0.099	-	mA	V _{GH} =15V
	I _{GL}	-	0.371	-	mA	V _{GL} = -7V
Input level of V1~V5	V _X	AV _{DD} /2-		AV _{DD} -0.1-	V	
Input level of V6~V10	V _X	0.1-		AV _{DD} /2-	V	

Note (1): HSYNC, VSYNC, DE, Digital Data

Note (2): Be sure to apply the power voltage as the power sequence spec.

Note (3): DGND=AGND=0V,)

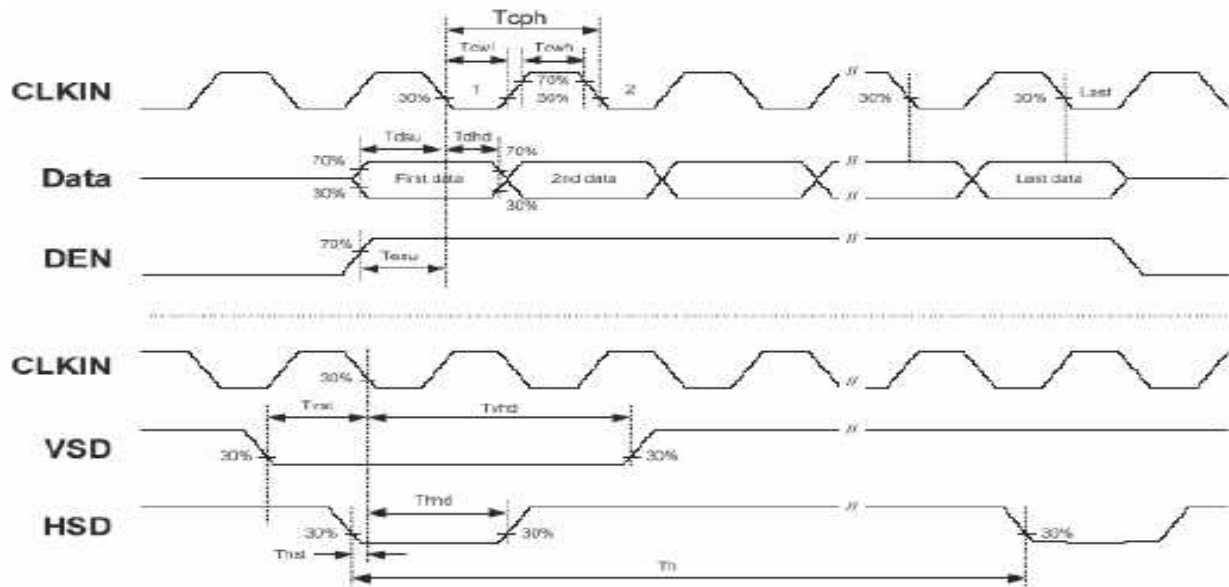
Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	13 / 25
Document No.		Revision	2.0

6.2 AC Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit	Note
DCLK cycle time	Tcph	25			ns	
DCLK frequency	fclk		30	40	MHz	
DCLK pulse duty	Tcwh	40	50	60	%	
VSD setup time	Tvst	8			ns	
VSD hold time	Tvhd	8			ns	
HSD setup time	Thst	8			ns	
HSD hold time	Thhd	8			ns	
Data setup time	Tdsu	8			ns	
Data hold time	Tdhd	8			ns	
DE setup time	Tesu	8			ns	
DE hold time	Tehd	8			ns	
Horizontal display area	thd		800		Tcph	
HSD period time	th		928		Tcph	
HSD pulse width	thpw	1	48		Tcph	
HSD back porch	thb		88		Tcph	
HSD front porch	thfp		40		Tcph	
Vertical display area	tvd		480		th	
VSD period time	tv		525		th	
VSD pulse width	tvpw		3		th	
VSD back porch	tvb		32		th	
VSD front porch	tvfp		13		th	

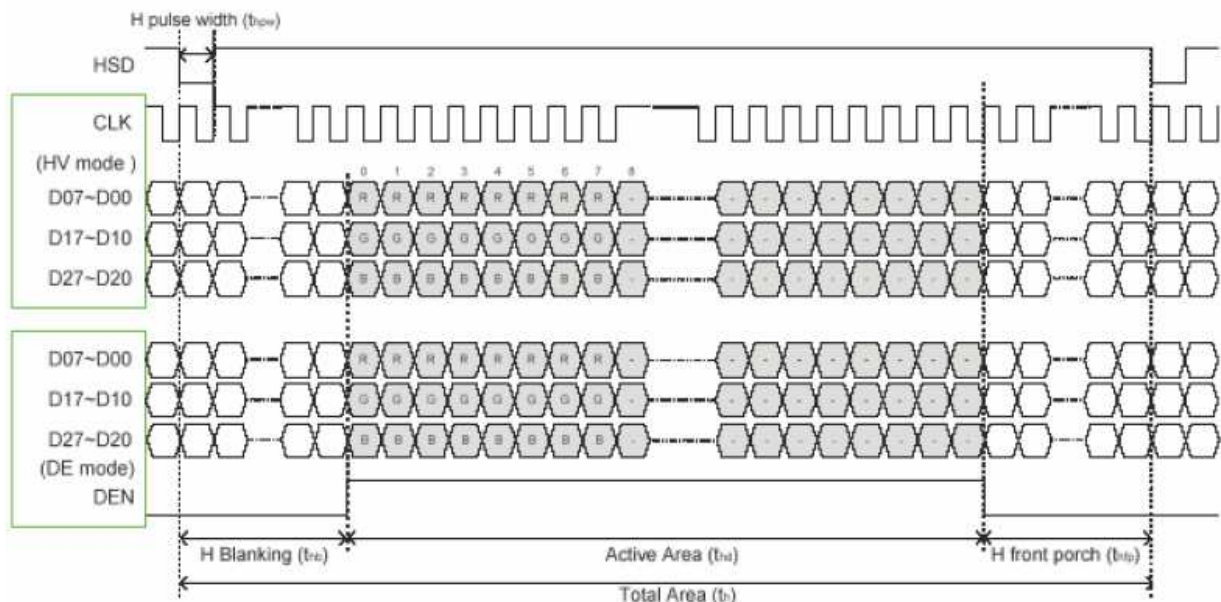
Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	14 / 25
Document No.		Revision	2.0

6.3 Timing Diagram of Interface Signal



Sampling clock timing

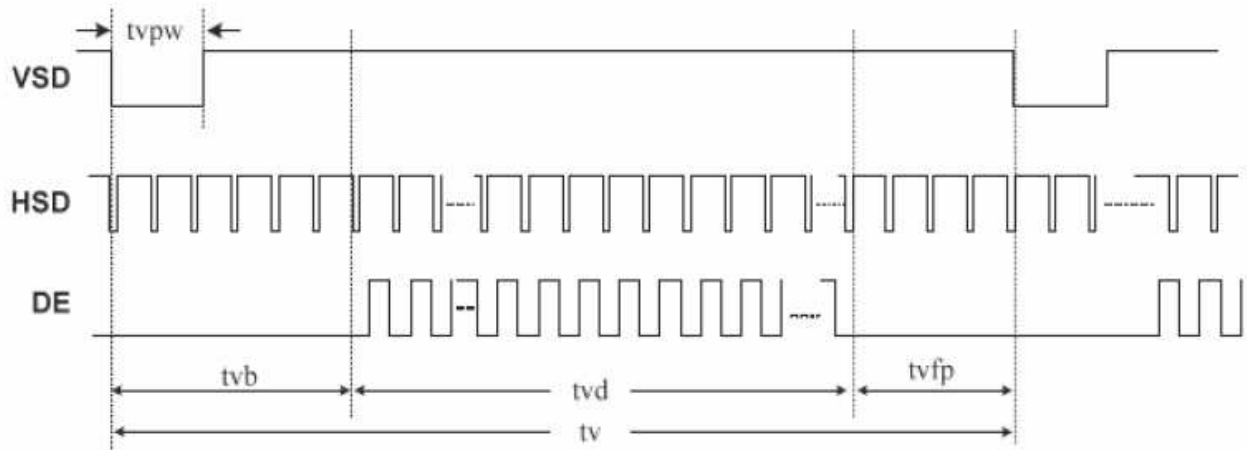
Horizontal input timing



Horizontal display timing range

Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	15 / 25
Document No.		Revision	2.0

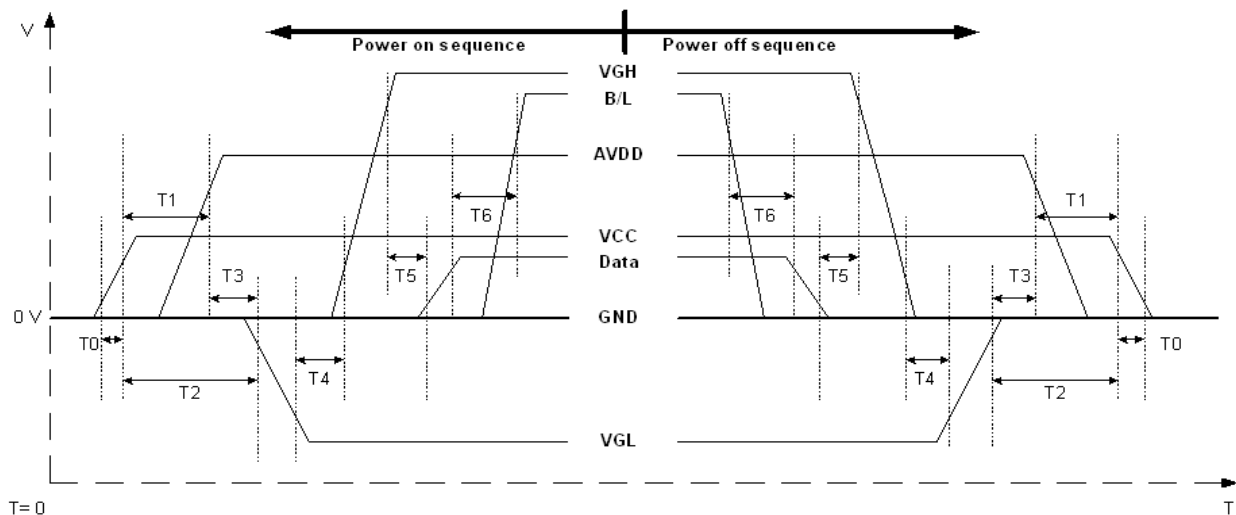
Vertical input timing



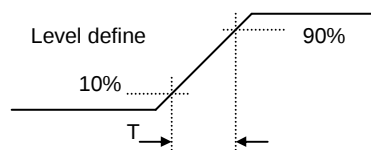
Vertical timing

Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	16 / 25
Document No.		Revision	2.0

6.4 Power Sequence



Item	Min.	Typ.	Max.	Unit
T0	0.5	--	20	msec
T1	16			msec
T2	20			msec
T3	0			msec
T4	20		--	msec
T5	20			msec
T6	50			msec



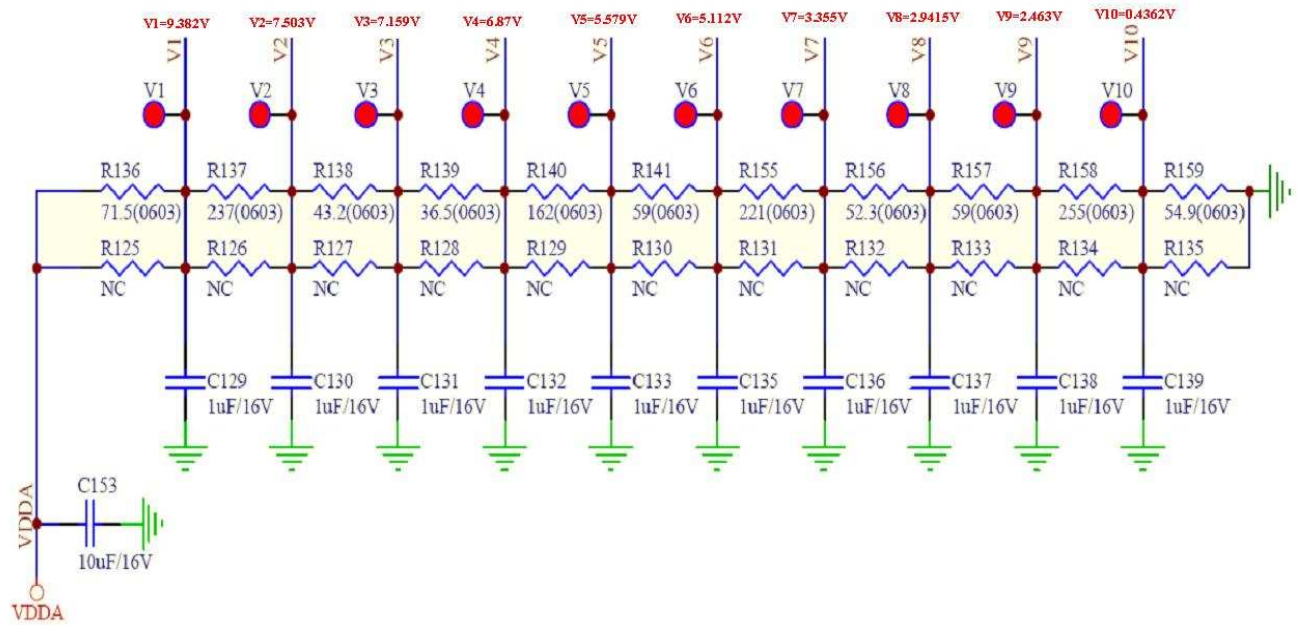
Power On Sequence: VCC-> AVDD -> VGL -> VGH -> Data -> B/L

Power Off Sequence: B/L-> Data -> VGH -> VGL -> AVDD -> VCC

Notes: Data include R0~R7, G0~G7, B0~B7, HSD, VSD, DCLK, SHLR, UPDN, DE MODE, RSTB, STBYB, SHLR, UPDN, DITH

Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	17 / 25
Document No.		Revision	2.0

6.5 Gamma circuit



Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	18 / 25
Document No.		Revision	2.0

7.0 Reliability test items

No.	Item	Conditions	Remark
1	High Temperature Storage	Ta=+80°C, 240hrs	
2	Low Temperature Storage	Ta=-30°C, 240hrs	
3	High Temperature Operation	Ta=+70°C, 240hrs	
4	Low Temperature Operation	Ta=-20°C, 240hrs	
5	High Temperature and High Humidity (operation)	Ta=+60°C, 90%RH, 240hrs	
6	Thermal Cycling Test (non operation)	-30°C(30min) → +80°C(30min), 200cycles	
7	Electrostatic Discharge	±200V,200pF(0Ω) 1 time/each terminal	
8	Packing	1. Sine, 1.5G, 5~200Hz, 1hr X, Y, Z direction 2. Random, 1.5Grms, 5~200Hz, 15min/ X, Y, Z direction 3. Half-Sine, 70G, 11ms+ X axis, 2 Times 4. Half-Sine, 200G, 2ms+ X axis, 2 Times 5. 90 degree topple to dash against the hard- face of table.	
9	Altitude Test (non operation)	50000ft, 24hr (25°C)	
10	Altitude Test (operation)	10000ft, 02hr (25°C)	
11	Pressure cooker Test	121°C, 100%R.H., 2atm, 16hr/20hr	

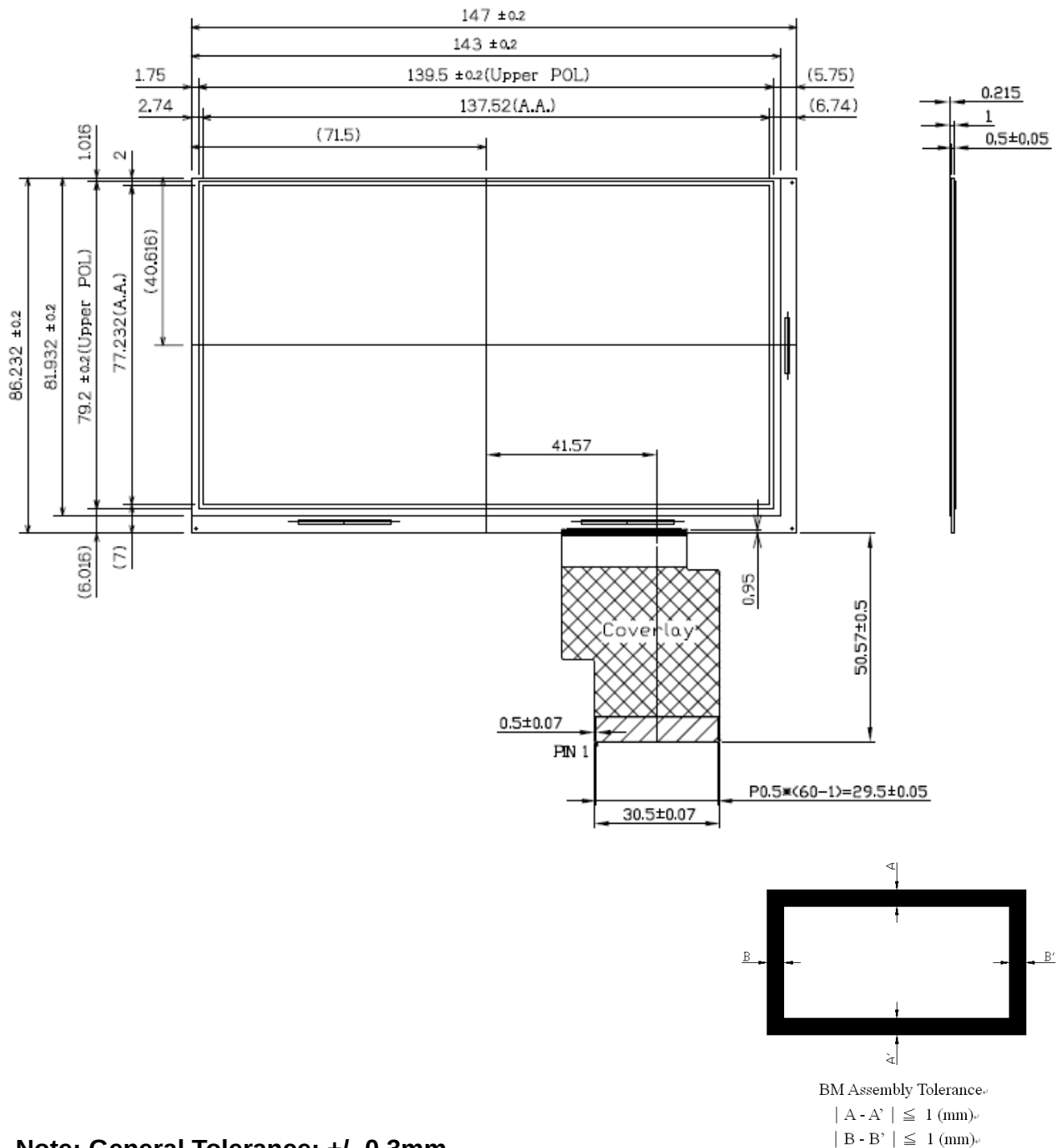
Note: There is no display function NG issue occurred, all the cosmetic specification is judged before the reliability stress.

Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	19 / 25
Document No.		Revision	2.0

8.0 OUTLINE DIMENSION

8.1 Outline Dimension

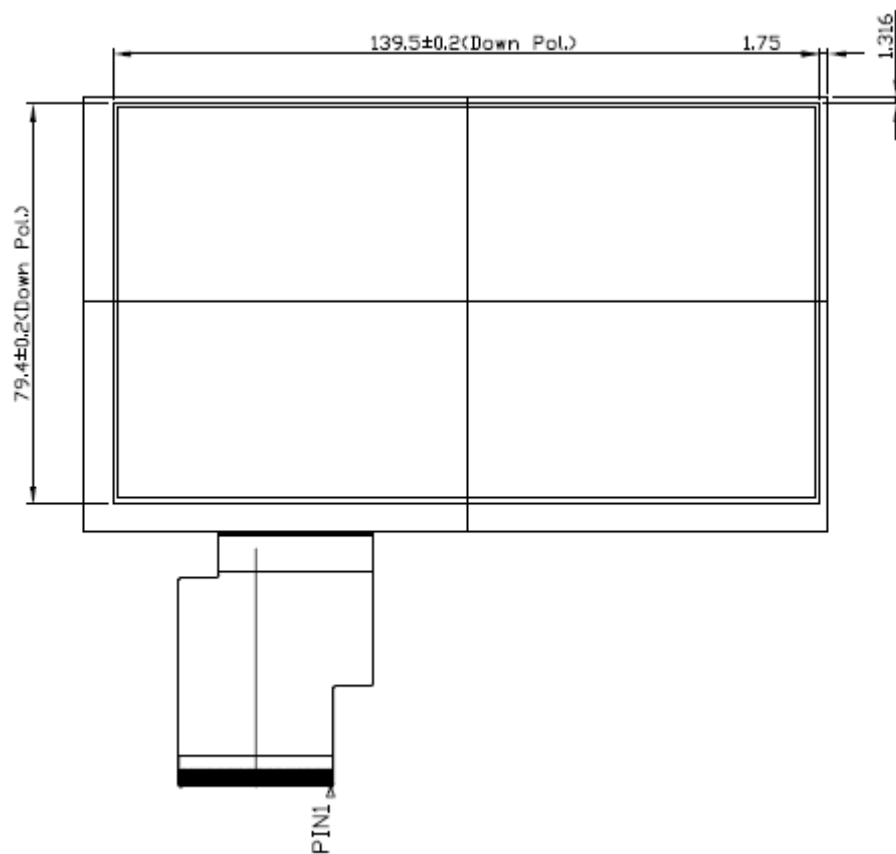
Unit : mm



Note: General Tolerance: +/- 0.3mm

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Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	20 / 25
Document No.		Revision	2.0



Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	21 / 25
Document No.		Revision	2.0

9.0 LOT MARK

9.1 Lot Mark

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
---	---	---	---	---	---	---	---	---	----	----	----	----	----	----

Code 1,2,3,4,5,6: HannStar internal flow control code.

Code 7: production location.

Code 8: production year.

Code 9: production month.

Code 10,11,12,13,14,15: serial number.

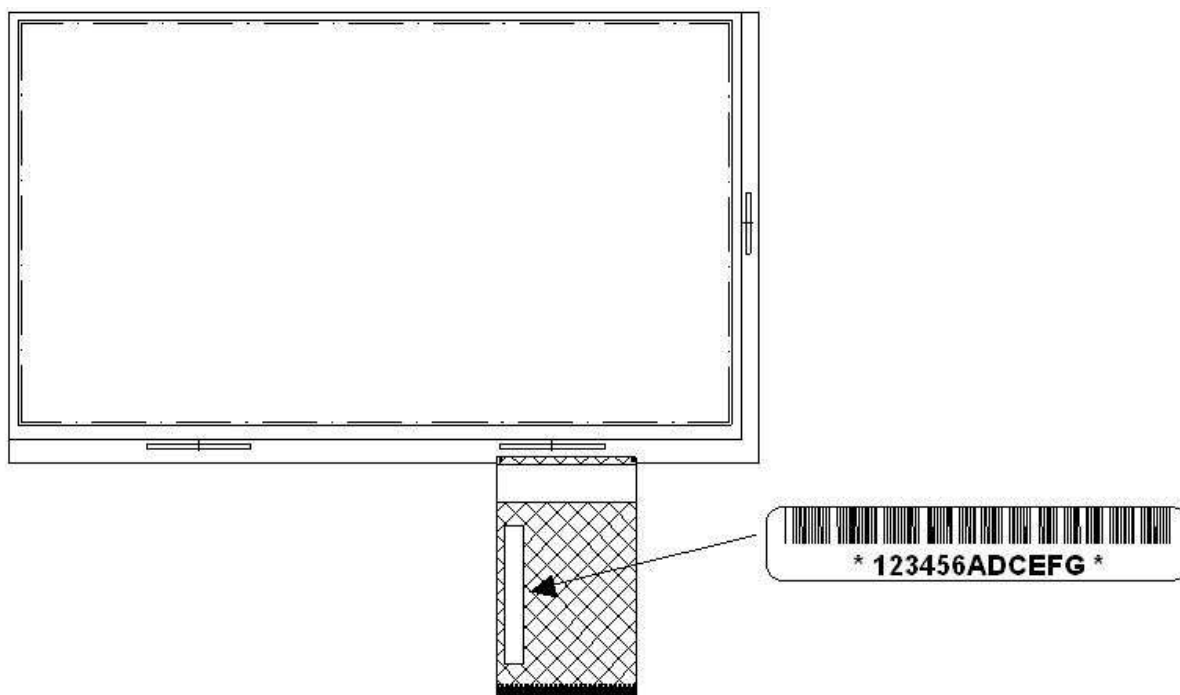
Note (1) Production Year: Code 8 is defined by the last number of the year, for example

Year	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015
Mark	6	7	8	9	0	1	2	3	4	5

Note (2) Production Month

Month	Jan.	Feb.	Mar.	Apr.	May.	Jun.	Jul.	Aug.	Sep.	Oct	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	A	B	C

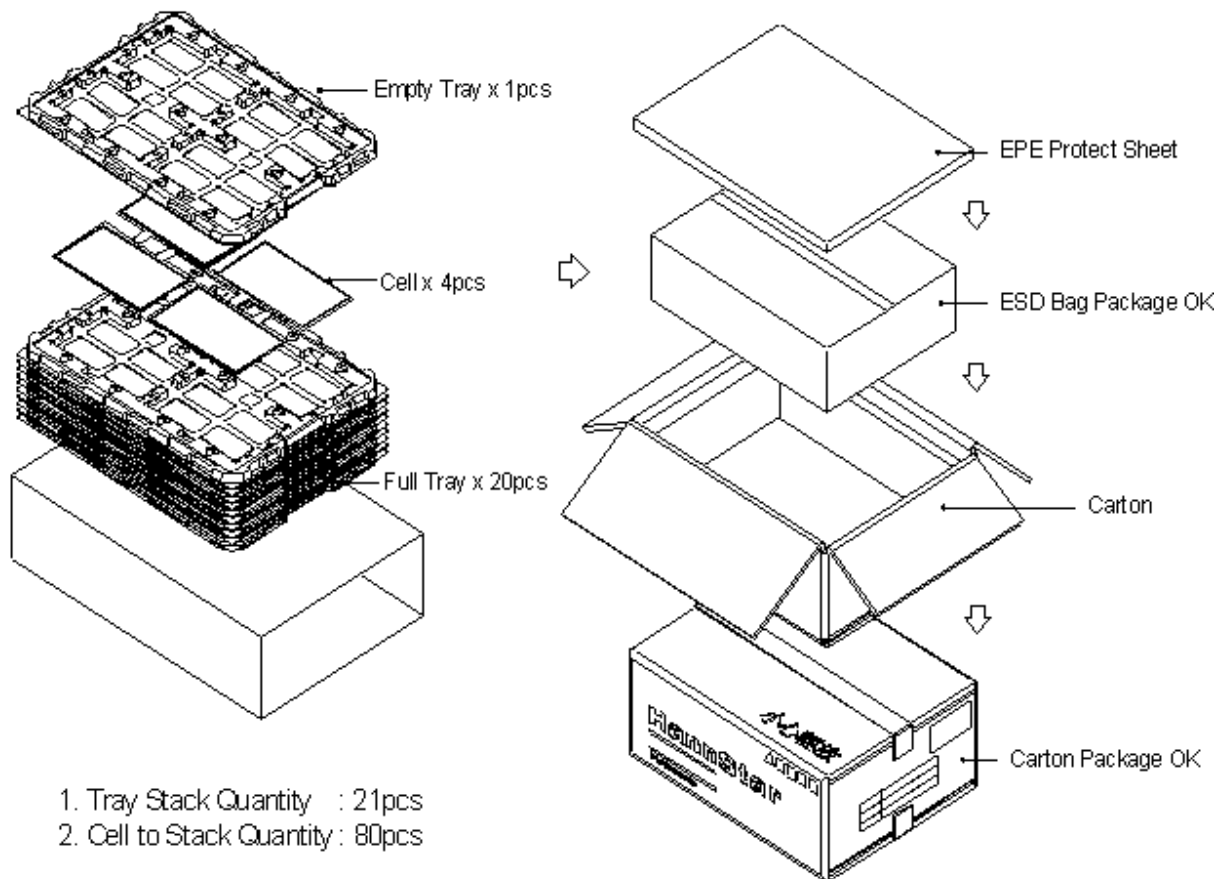
9.2 Location of Lot Mark



Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	22 / 25
Document No.		Revision	2.0

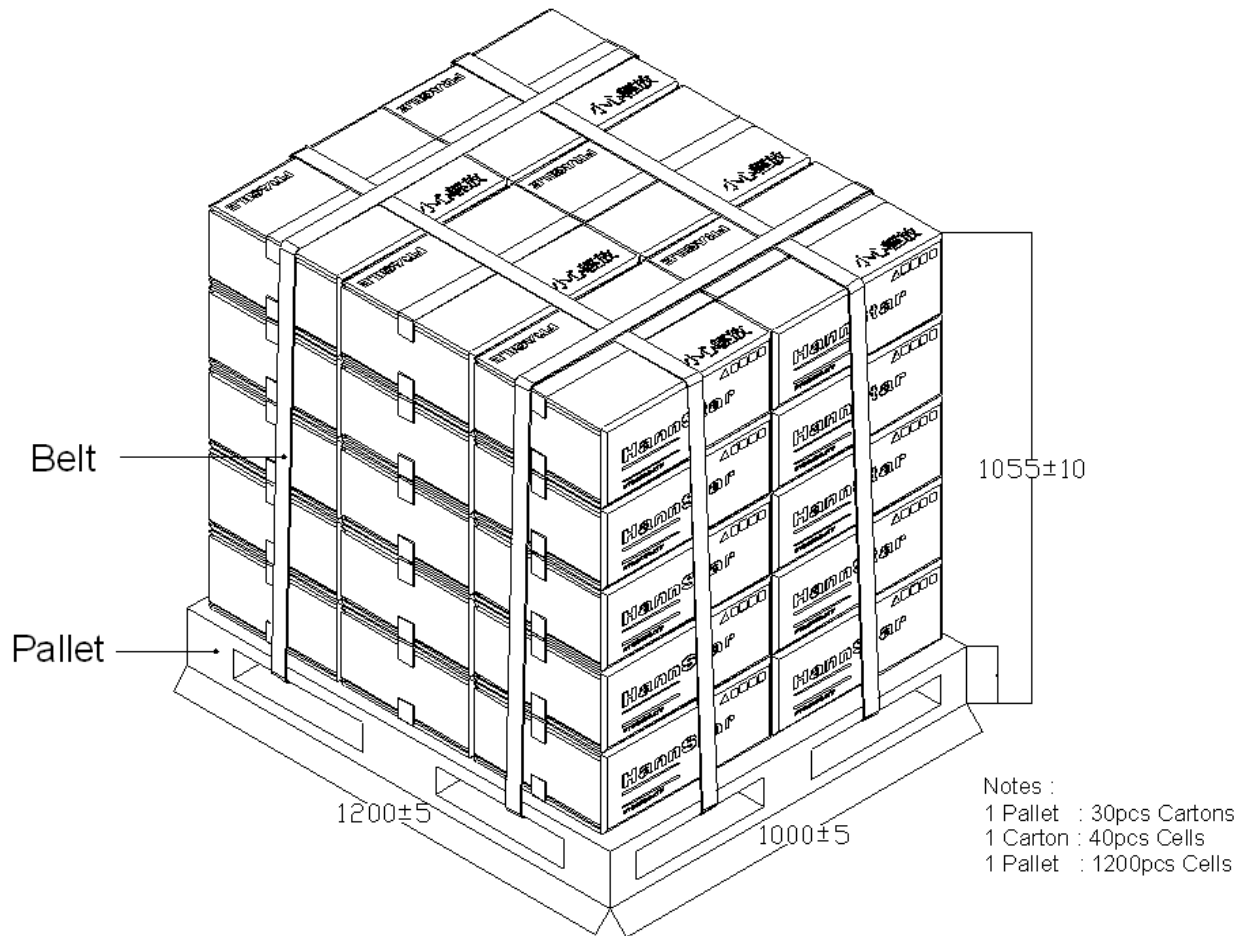
10.0 PACKAGE SPECIFICATION

10.1 6.2 inch Open Cell Package (40 in 1)



Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	23 / 25
Document No.		Revision	2.0

10.2 Pallet drawing



Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	24 / 25
Document No.		Revision	2.0

11.0 GENERAL PRECAUTION

11.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life threatening or otherwise catastrophic.

11.2 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. HannStar does not warrant the module, if customers disassemble or modify the module.

11.3 Breakage of LCD Panel

11.3.1. If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.

11.3.2. If liquid crystal contacts mouth or eyes, rinse out with water immediately.

11.3.3. If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.

11.3.4. Handle carefully with chips of glass that may cause injury, when the glass is broken.

11.4 Electric Shock

11.4.1. Disconnect power supply before handling LCD module.

11.4.2. Do not pull or fold the LED cable.

11.4.3. Do not touch the parts inside LCD modules and the fluorescent LED's connector or cables in order to prevent electric shock.

11.5 Absolute Maximum Ratings and Power Protection Circuit

11.5.1. Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.

11.5.2. Please do not leave LCD module in the environment of high humidity and high temperature for a long time.

11.5.3. It's recommended to employ protection circuit for power supply.

11.6 Operation

11.6.1 Do not touch, push or rub the polarizer with anything harder than HB pencil lead.

11.6.2 Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.

11.6.3 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.

Document Title	HSD062IDW1-A**-0299 Formal Specification	Page No.	25 / 25
Document No.		Revision	2.0

11.6.4 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color fading.

11.6.5 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzine or other adequate solvent.

11.7 Mechanism

Please mount LCD module by using mouting holes arranged in four corners tightly.

11.8 Static Electricity

11.8.1 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.

11.8.2. Because LCD module use CMOS-IC on circuit board and TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge. Persons who handle the module should be grounded through adequate methods.

11.9 Strong Light Exposure

The module shall not be exposed under strong light such as direct sunlight. Otherwise, display characteristics may be changed.

11.10 Disposal

When disposing LCD module, obey the local environmental regulations.